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**INFORMATION
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To Commissioner For Patents

Enclosed herewith is a Form PTO-1449, required copies of documents listed thereon, and a concise explanation of their relevance is described below or enclosed herewith per 37 CFR 1.97.

Application Number	09/730,659
Filing Date	December 6, 2000
First Named Inventor	Benoit Veillette
Group Art Unit	2631
Examiner Name	
Attorney Docket Number	US 000295

These documents may be relevant in that they have been:

- ☐ considered in drafting the specification of the above referenced application;
- ☐ cited in the specification of the above-referenced application;
- ☐ previously submitted or cited in U.S. patent application(s) _____ which are relied on for an earlier effective filing date under 35 U.S.C. 120 (no copy required); or
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Signature		Date	3/14/03

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Form PTO-1449
U.S. DEPARTMENT OF
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(REV. 7-80) PATENT AND TRADEMARK OFFICEAtty. Docket No.
US 000295Serial No.
09/730,659Applicant
BENOIT VEILLETTEFiling Date
DECEMBER 6, 2000Group
2631**INFORMATION DISCLOSURE CITATION**
(Use several sheets if necessary)**U.S. PATENT DOCUMENTS**

Ex. Int	Document Number	Date	Name	Class	Sub-class	Filing Date If Approp.
AA	6 1 1 8 8 1 1	07/1997	NARUMI et al	375	219	
AB						
AC						
AD						
AE						
AF						

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FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Sub-class	Trans.	
						Yes	No
AG							
AH							
AI							
AJ							

OTHER (Including Author, Title, Date, Pertinent Pages, Etc.)

AL	B. DUFORT ET AL; "SIGNAL GENERATION USING PERIODIC SINGLE AND MULTI-BIT SIGMA-DELTA MODULATED STREAMS", PROCEEDINGS OF THE INTERN. TEST CONF., ITC'97, WASH,DC, 11/1-6/1997, INTERN. TEST CONF, NY, IEEE, VOL. CONF, 28, 11/1/1997, PAGES 396-405, XP000800336.
AM	G.W. ROBERTS ET AL; "MAKING COMPLEX MIXED-SIGNAL TELECOMMUNICATION INTEGRATED CIRCUITS TESTABLE", IEEE COMMUNICATIONS MAGAZINE, IEEE SERVICE CENTER, PISCATAWAY, NJ, VOL. 37, NO. 6, 06/1999, PAGES 90-96, XP000835284.
AN	F. PICHON; "TESTABILITY FEATURES FOR A SUBMICRON VOICE-CODER ASIC", PROCEEDINGS, INTERN. TEST CONF. 1996. TEST AND DESIGN VALIDITY (IEEE CAT. NO. 96CH35976), TEST AND DESIGN VALIDITY, WASH, DC, 10/20-25/1996, PAGES 377-385, XP002228502.
AO	C.A. PAPACHRISTOU ET AL; "MICROPROCESSOR BASED TESTING FOR CORE-BASED SYSTEM ON CHIP", DESIGN AUTOMATION CONFERENCE, 1999. PROCEEDINGS. 36 TH NEW ORLEANS, LA, 06/21-25/1999, PISCATAWAY, NJ, PAGES 586-591, XP010344097.

Examiner

Date Considered

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